Simulate the behavior of the oscillator of problem 3-10 using SPICE. Use the standard op-amp model (in the analog library), which has output limits at ±15 V. As shown in the notes, you will need to include a voltage or current pulse somewhere in the circuit to "kick off" the oscillation. Run the simulation three times, first for the case where R_2/R_1 is 10% too small to cause sustained oscillation, secondly when R_2/R_1 is exactly the value needed to cause oscillations, and thirdly when R_2/R_1 is 10% bigger than minimum needed to start oscillations.